



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CDST-C122-2P

Inventor(s) Curtain et al.

Serial No.: 09/773,983

Group Art Unit: Not yet assigned

Filed: 1/31/01

Examiner: Not yet assigned

Title: DUAL LAYER ELECTROPLATED STRUCTURE FOR A FLAT PANEL DISPLAY DEVICE

The Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
5,972,193	METHOD OF MANUFACTURING A PLANAR COIL USING A TRANSPARENCY SUBSTRATE	10/26/99
5,989,404	METHOD FOR MANUFACTURING A FLUORESCENT SCREEN DISPLAY	11/23/99
5,818,153	SELF-ALIGNED GATE FIELD EMITTER DEVICE AND METHODS FOR PRODUCING THE SAME	10/6/98
5,725,787	FABRICATION OF LIGHT-EMITTING DEVICE WITH RAISED BLACK MATRIX FOR USE IN OPTICAL DEVICES SUCH AS FLAT-PANEL CATHODE-RAY TUBES	3/10/98
5,098,860	METHOD OF FABRICATING HIGH-DENSITY INTERCONNECT STRUCTURES HAVING TANTALUM/TANTALUM OXIDE LAYERS	3/24/92
5,827,099	USE OF EARLY FORMED LIFT-OFF LAYER IN FABRICATING GATED ELECTRON-EMITTING DEVICES	10/27/98


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Respectfully submitted,

Date: 4/23/01

By: 
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